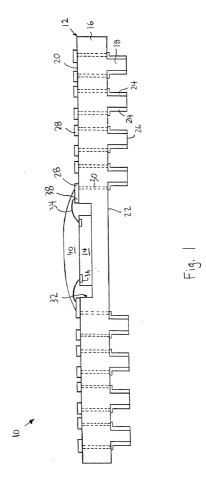
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Fig. 2

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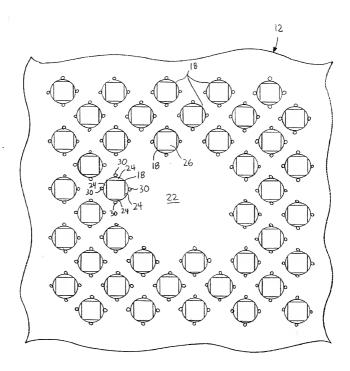
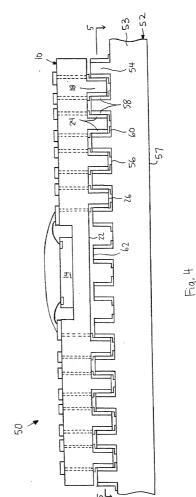


Fig. 3



Title of Invention: Integrated Circuit Package Including Interconnection Posts For Multiple Electrical
Connections

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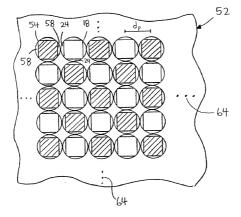


Fig. 5

Connections

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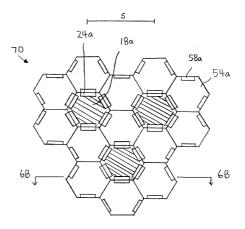
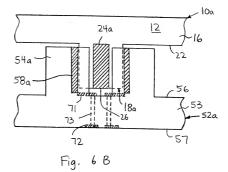
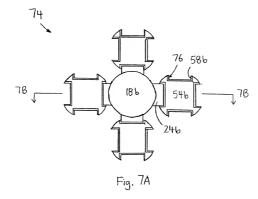


Fig. 6A



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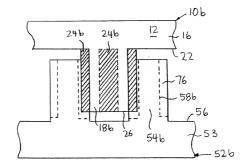


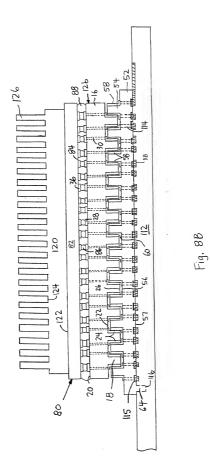
Fig. 7B

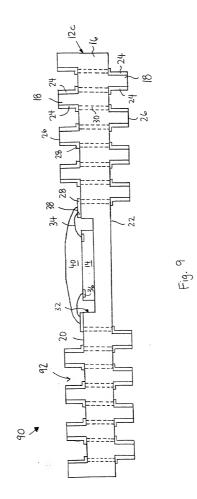
Title of Invention: Integrated Circuit Package Including Interconnection Posts For Multiple Electrical Connections

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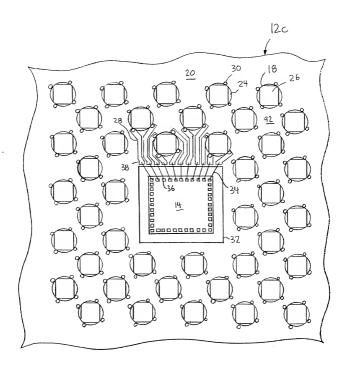
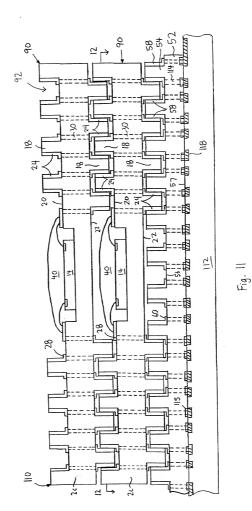


Fig. 10

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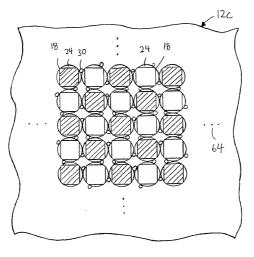
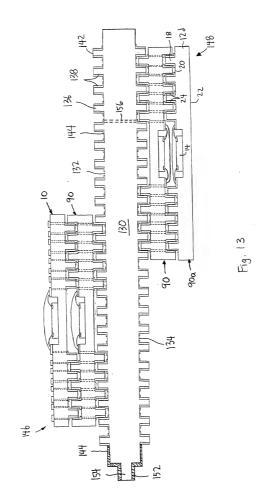


Fig. 12



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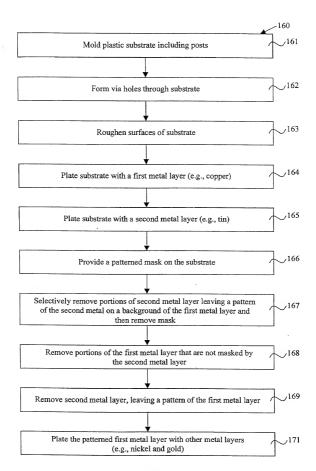


Fig. 14

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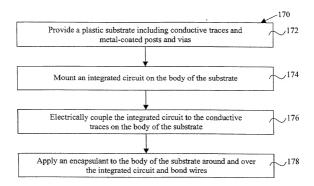


Fig. 15

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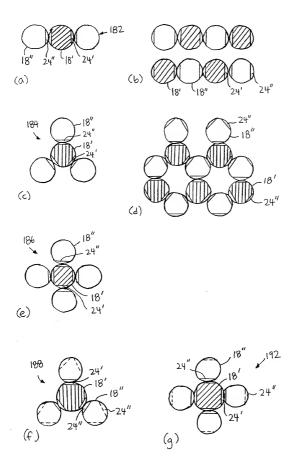


Fig. 16

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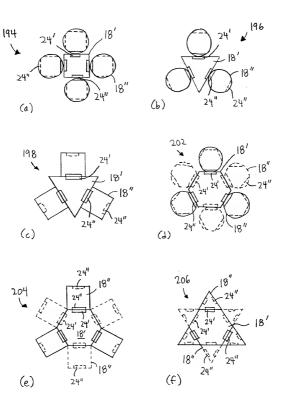


Fig. 17